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PTO/SAB/08A (08-01)
Approved for use through 10/31/2002, OMB 0851-0031
GSA FPMR (41 CFR) - 101-11, 101-11.2, 101-11.2, 101-11.2.2

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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				<p>Complete if Known</p> <table border="1"> <tr> <td>Application Number</td> <td>09/899,977</td> </tr> <tr> <td>Filing Date</td> <td>6 July 2001</td> </tr> <tr> <td>First Named Inventor</td> <td>Keeth, et al</td> </tr> <tr> <td>Group Art Unit</td> <td>2818 2827</td> </tr> <tr> <td>Examiner Name</td> <td>Not Yet Assigned M. TRAN</td> </tr> </table>		Application Number	09/899,977	Filing Date	6 July 2001	First Named Inventor	Keeth, et al	Group Art Unit	2818 2827	Examiner Name	Not Yet Assigned M. TRAN
Application Number	09/899,977														
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First Named Inventor	Keeth, et al														
Group Art Unit	2818 2827														
Examiner Name	Not Yet Assigned M. TRAN														
Sheet	1	of	1	Attorney Docket Number	DB000575-015										

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/Michael Tran/	Date Considered	06/15/2007
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Appr. 4 for use through 10/31/2002 OMS 0651-0031
Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Application Number	Not yet assigned 09/899977
Filing Date	July 6, 2001
First Named Inventor	Keeth et al.
Group Art Unit	Not yet assigned 2827
Examiner Name	Not yet assigned M. IRAN
Attorney Docket Number	DB000575-015

U.S. PATENT DOCUMENTS

Examiner Initials'	Cite No.'	U.S. Patent Document Number	Kind Code* (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/MT/		6,043,118		Suwanai et al.	03/28/2000	
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Signature**

/Michael Tran/

Date _____

06/15/2007

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 2 of 3

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Application Number	Not yet assigned	09/899977
Filing Date	July 6, 2001	
First Named Inventor	Keeth et al.	
Group Art Unit	Not yet assigned	2827
Examiner Name	Not yet assigned M. TRAN	
Attorney Docket Number	DB000575-015	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2
/MT/		SUGIBAYASHI ET AL., "A 30-ns 266-Mb DRAM with a Multidivided Array Structure", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993)	
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		KITSUKAWA ET AL., "256-Mb DRAM Circuit Technologies for File Applications", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993)	
		JEDEC SOLID STATE PRODUCTS ENGINEERING COUNCIL, "Committee Letter Ballot", JC-42.3-95-73, Item #633.13, Arlington, VA (April 1995)	
		YOO ET AL., "SP 23.6: A 32-Bank 1Gb DRAM with 1 GB/s Bandwidth", ISSCC96/Session 23/DRAM/PAPER SP 23.6	
		NITTA ET AL., "SP 23.5: A 1.5GB/s Data-Rate 1Gb Synchronous DRAM with Hierarchical Square-Shaped Memory Block and Distributed Bank Architecture", ISSCC96/Session 23/DRAM PAPER SP 23.5	
		U.S. Patent Application S.N. 08/521,563, entitled IMPROVED VOLTAGE REGULATOR CIRCUIT, filed 8/30/95	
		U.S. Patent Application S.N. 08/688,347, entitled DIFFERENTIAL VOLTAGE REGULATOR, filed 6/26/96	
		U.S. Patent Application S.N. 08/460,234, entitled SINGLE DEPOSITION LAYER METAL DYNAMIC RANDOM ACCESS MEMORY, filed 8/17/95	
		U.S. Patent Application S.N. 08/420,943, entitled DYNAMIC RANDOM-ACCESS MEMORY, filed 6/4/95	
↓			
/MT/		U.S. Patent Application S.N. 08/194,184, entitled INTEGRATED CIRCUIT POWER SUPPLY HAVING PIECEWISE LINEARITY, filed 2/8/94	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 3 of 3

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